

ABSTRACT

A deferred merging router. Partial feasible routing solutions corresponding to each of a subset of a set of wires to be routed are identified.

the partial feasible routing solutions are then merged to identify one or more

- 5 feasible routing solutions for the set of wires to be routed. In one aspect of the invention, the final routing for an integrated circuit device may then be selected from the feasible routing solutions which may be ordered by one or more user-selected cost functions.